

Fig. 16

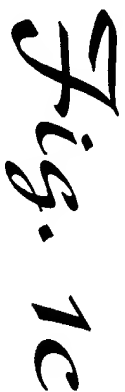


Fig. 1d

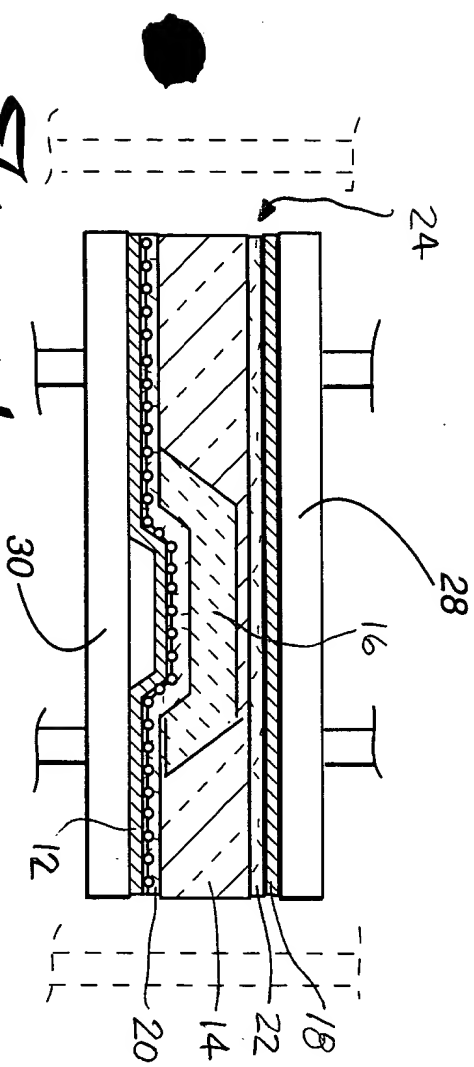


Fig. 1e

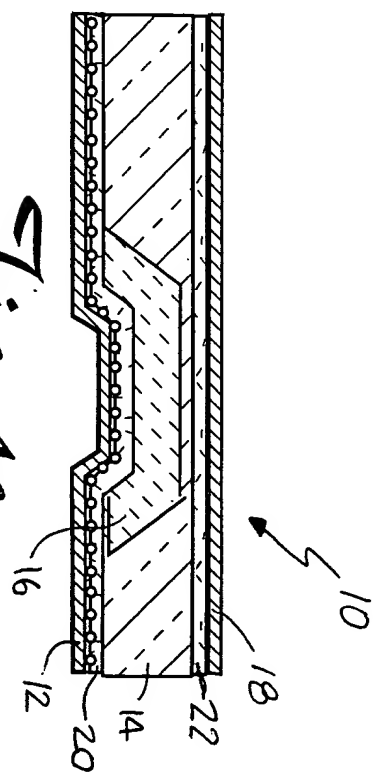


Fig. 2a

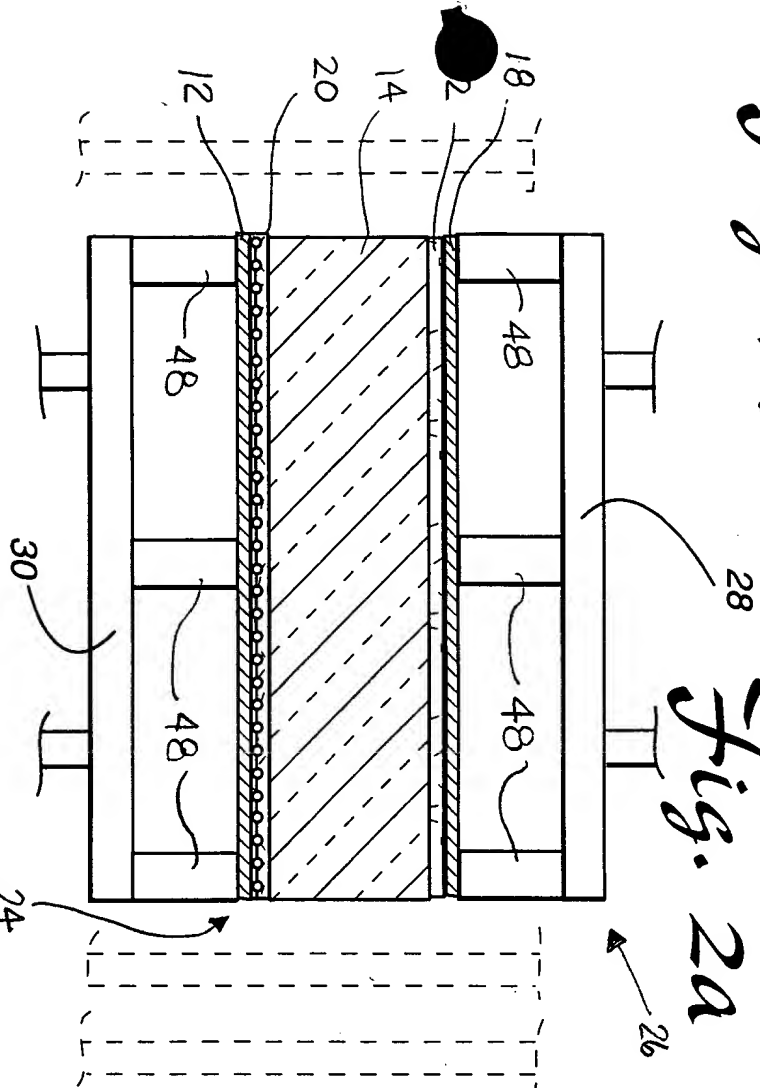


Fig. 2b

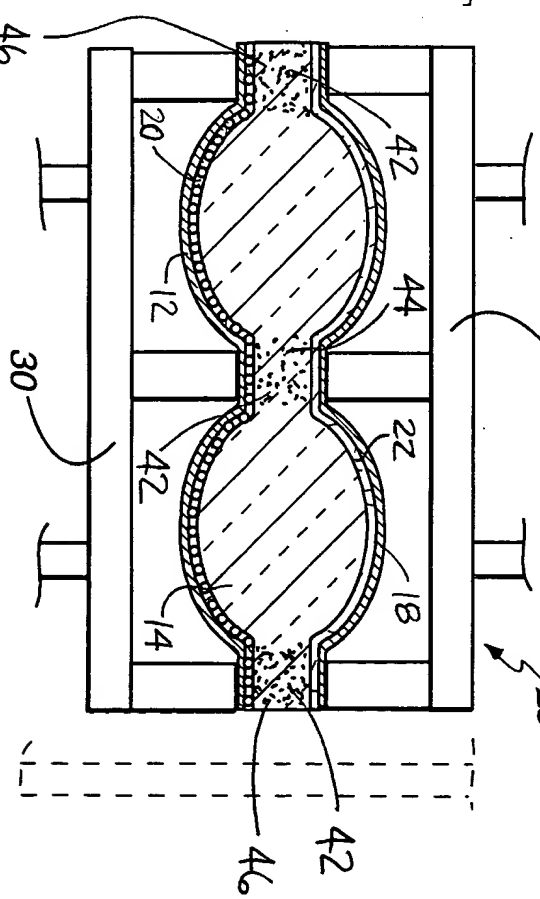


FIG. 1d is a cross-sectional view of a device assembly 10, showing a substrate 28 with a top layer 16 and a bottom layer 12. A central layer 14 is shown with a dashed line 18 indicating a boundary. A dashed line 20 is also present. A dashed line 24 is shown on the right side.